An Analysis of route reflector performance in I-BGP

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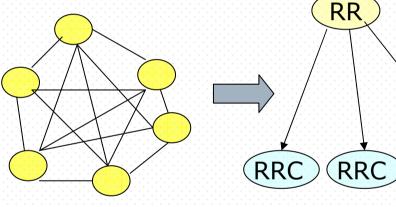
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Background(1)

□ I-BGP

- Requires synchronization with all I-BGP routers
 - □ Full mesh
- ➡ Lack of scalability



I-BGP fullmesh

Route reflector

➡ Introduction of Route Reflector(RR)

RRC

Background(2)

- □ What if RR is outage?
 - RRCs lost connectivity
 - single point of failure
 - ISP requires 24 hours x 365
 - Requirement for redundancy
- □ Introduction of Backup RR
 - RRC establishes BGP peer with both RR-1/RR-2
 - RRC receives an exact routing information both from RR-1,RR2
 - Hierarchal Route Reflector Model

PRR-

RR-2

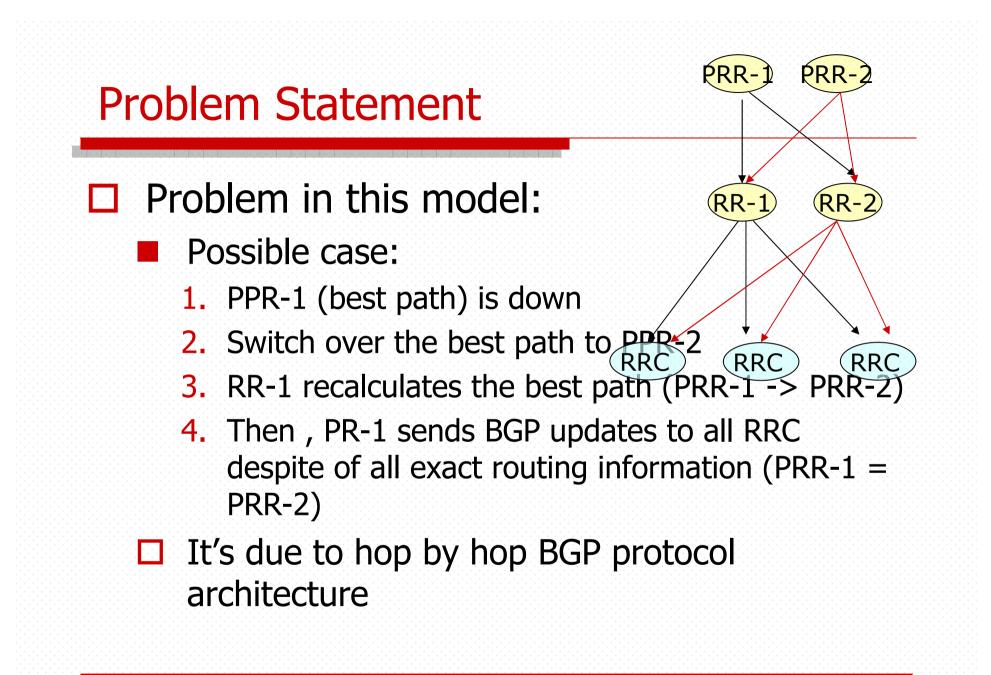
RRC

PRR-

RR-

RRC

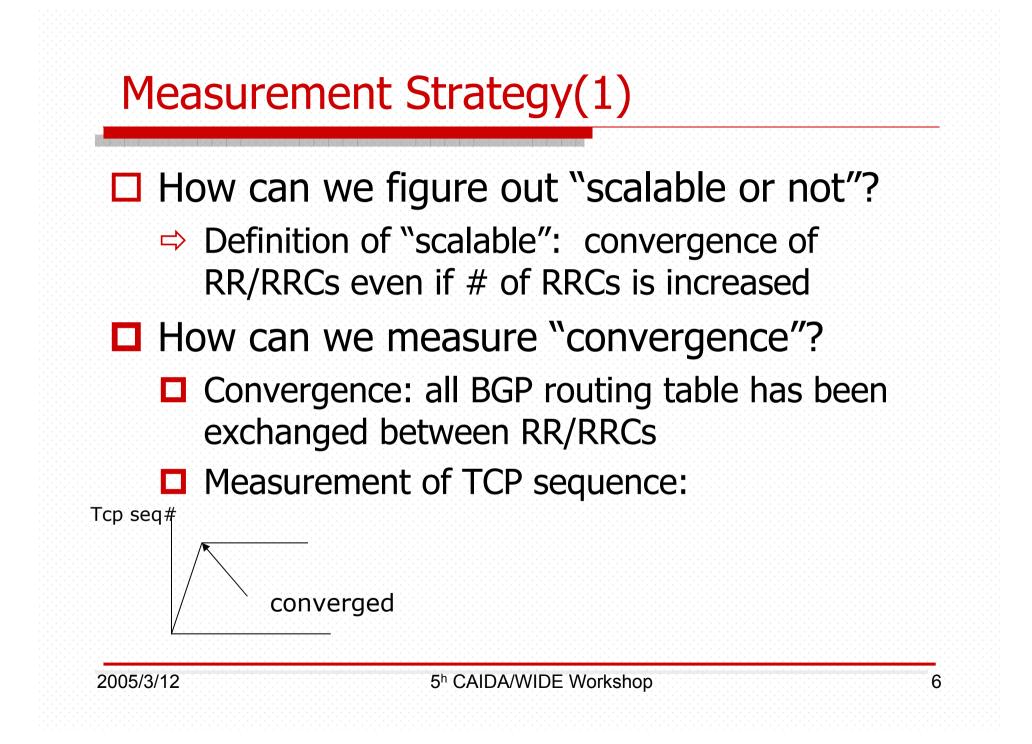
RRC

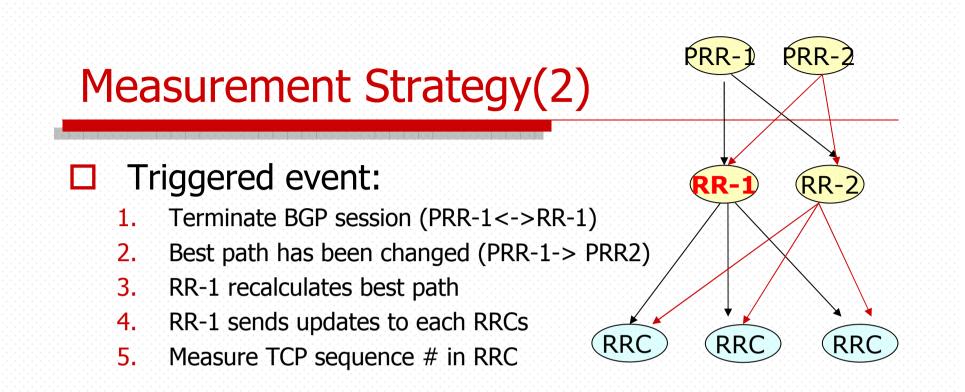


Motivation

? Is this redundant route reflector architecture truly scalable?

- □ How much RRCs can RR accommodate?
 - **10, 100, 1000?**
- What is the main elements which affect a performance of scalability?
 - # of routing information , e.g. fullroute (over 150,000)
 - BGP attribute?
 - Router implementation?

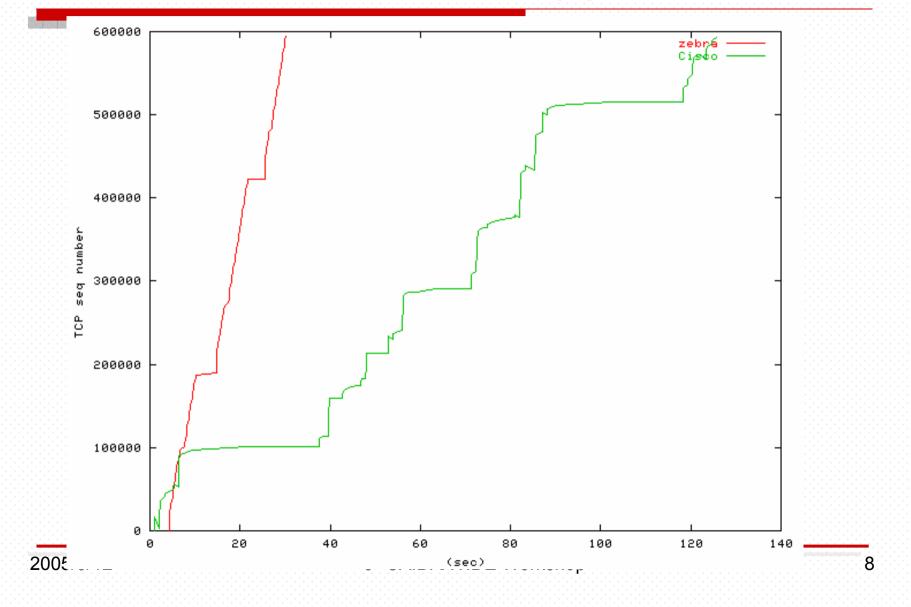


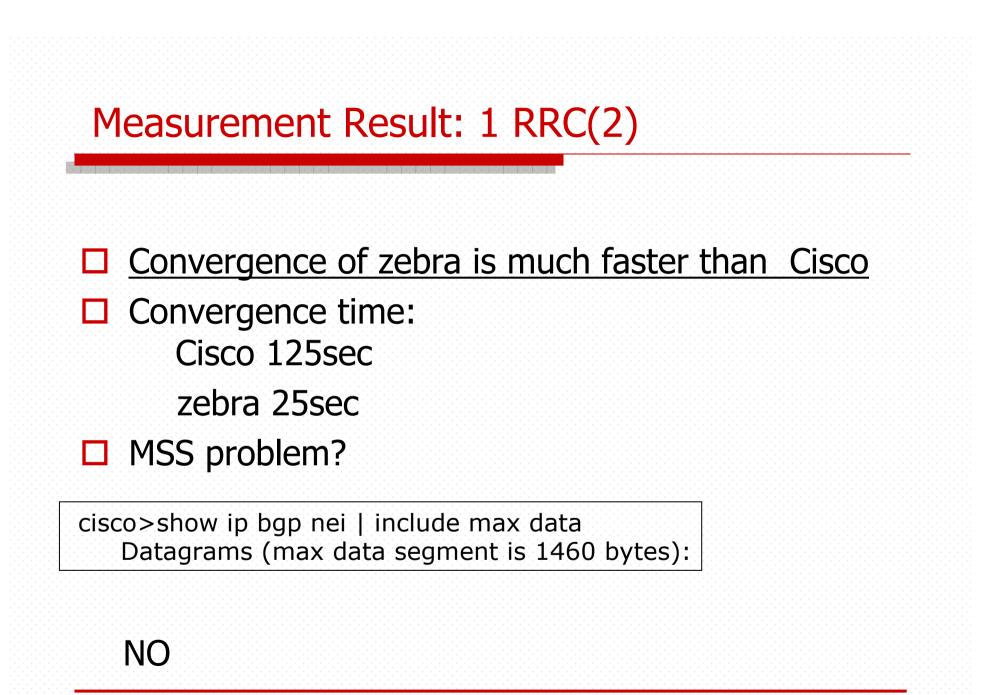


Parameters:

- **1.** BGP table \Rightarrow full route (146,955prefix/32000 attributes)
- **2.** RRClient \Rightarrow 1,30,60,170 RRCs (starbed)
- 3. Implementatin \Rightarrow zebra (FreeBSD4.10, memory 512MB) Cisco(IOS12.2(24a)) 256MB FE as RR-1

Measurement Result: 1 RRC(1)

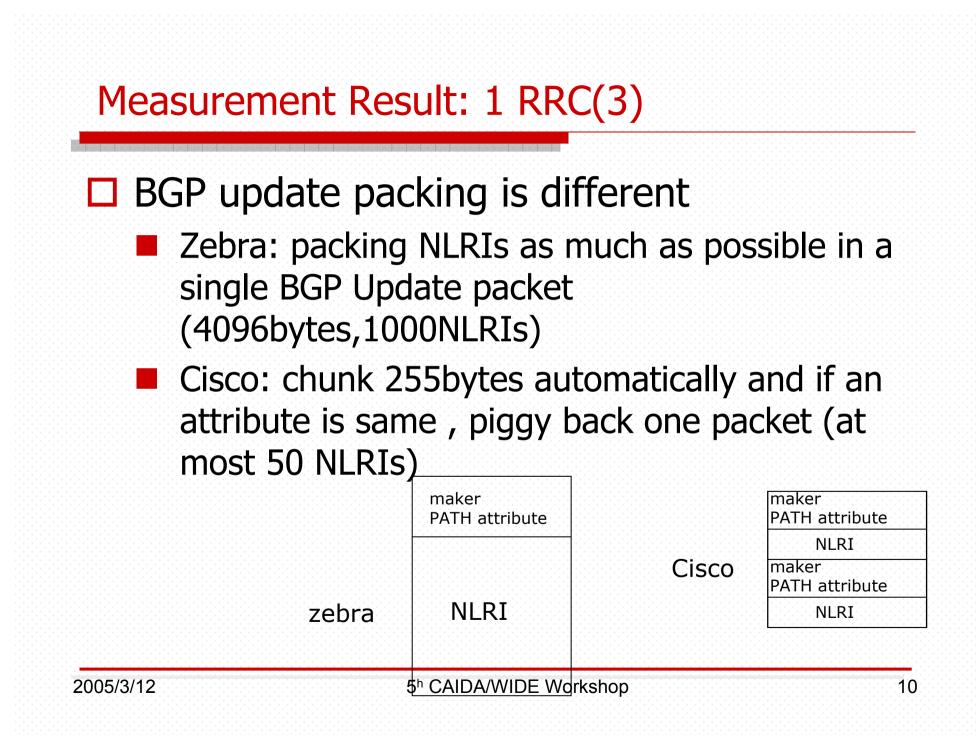




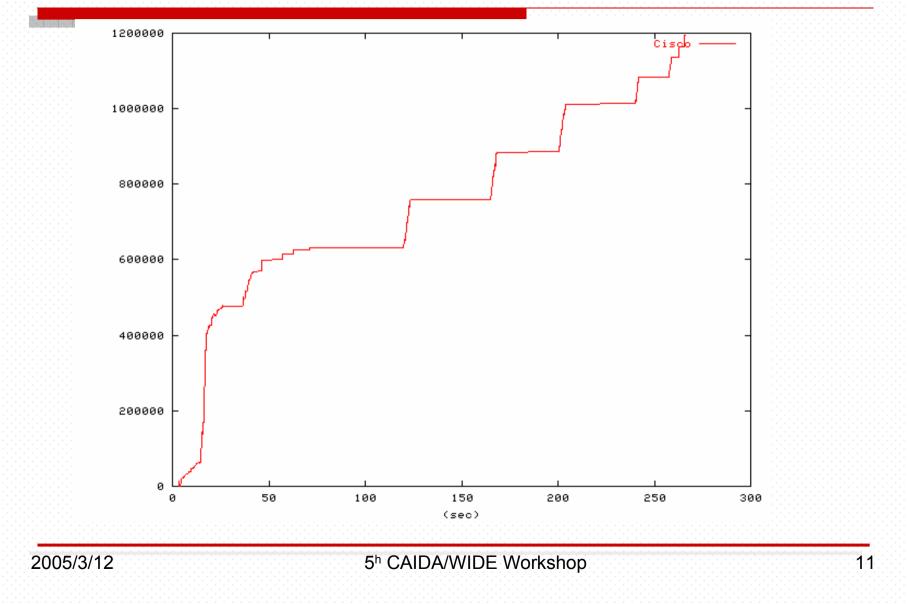
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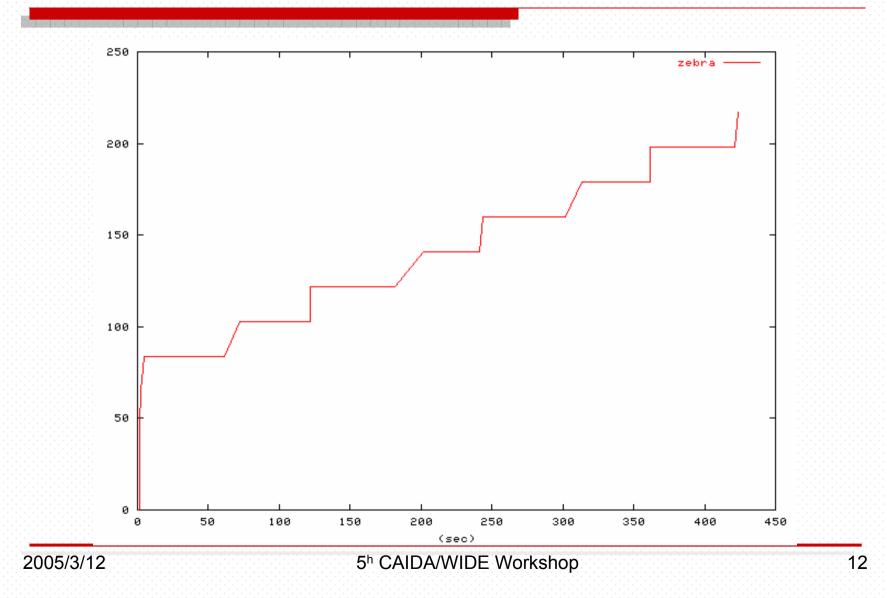
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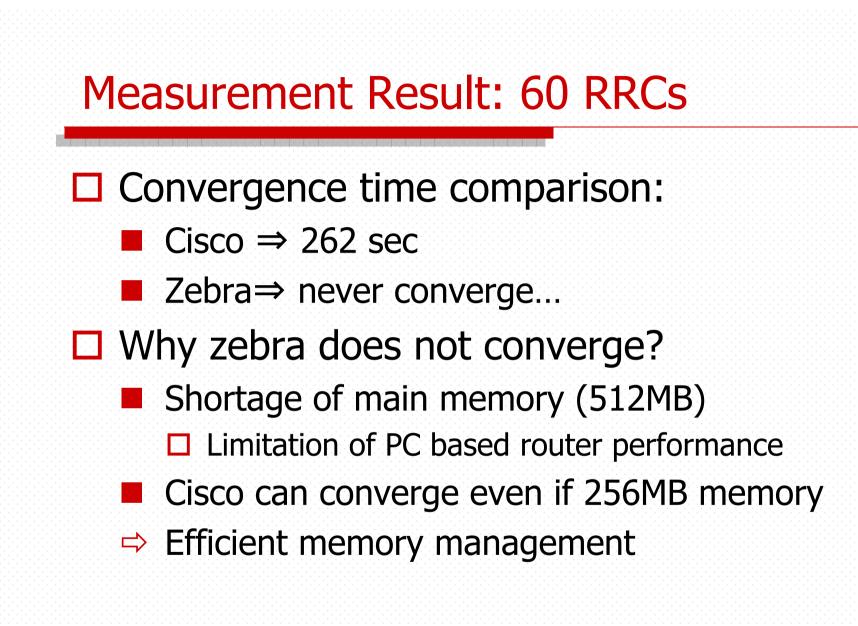


Measurement Result: 60 RRCs (Cisco)



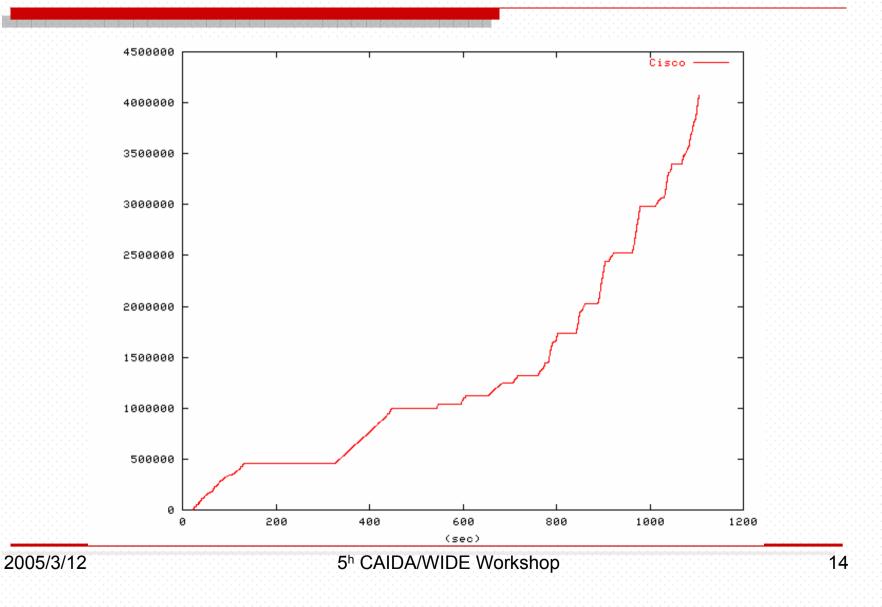
Measurement Result: 60 RRCs (zebra)

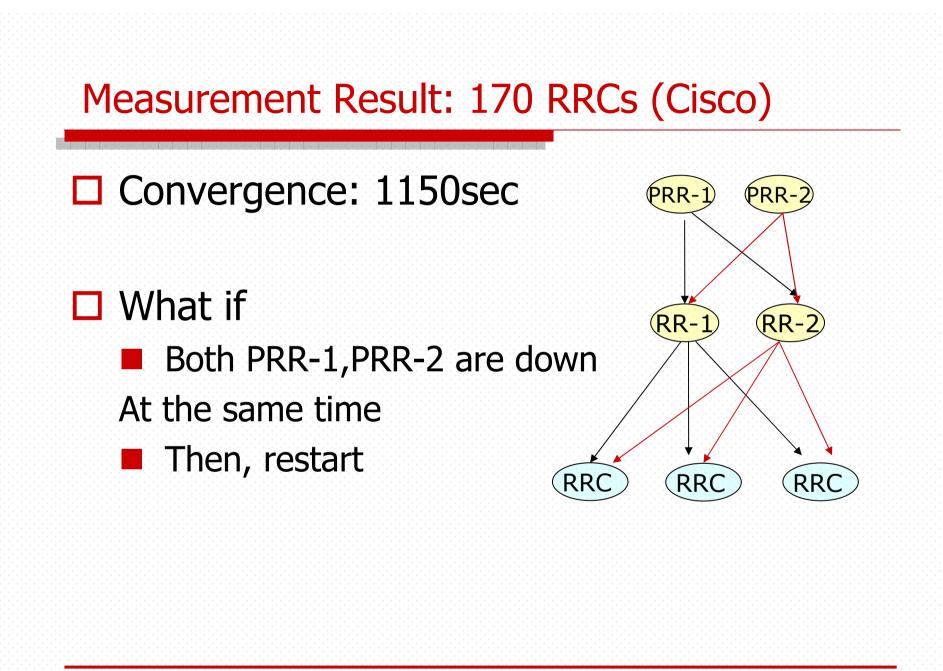




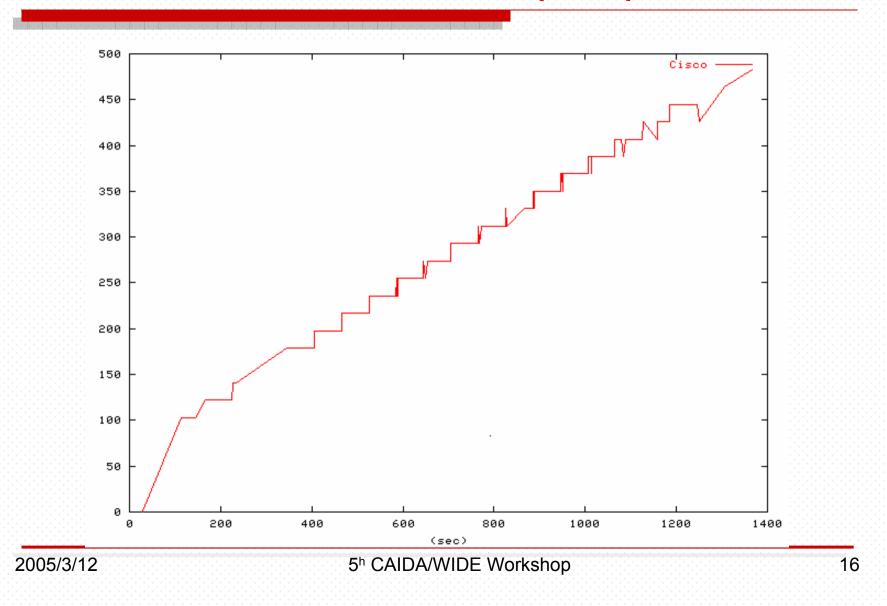


Measurement Result: 170 RRCs (Cisco)





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Never converged:	Neighbor	V AS MsgR	lcvd	MsgSe	nt I	InQ OutQ
	172.16.0.62	4 65535	9	44744	0	291
	172.16.0.63	4 65535	9	46217	0	319
	172.16.0.64	4 65535	9	46310	0	724
	172.16.0.65	4 65535	9	37370	0	169
	172.16.0.66	4 65535	9	46374	0	665
	172.16.0.67	4 65535	9	23387	0	125
	172.16.0.68	4 65535	9	19541	0	0
	172.16.0.69	4 65535	9	32036	0	0
□ Why?	172.16.0.70	4 65535	9	22729	0	306
high overload in RR-1						

- Receive from both PRR-1,2 and Send update to RRC x 170
- □ Limitation of CPU processing
- Missing BGP update packet processing
- Never finalize sending BGP update
- Stack output queue

Conclusion

? Is this redundant route reflector architecture truly scalable?

- When physical threshold turns over, it is never converged
 - Hierarchal Redundant RR architecture provide poor scalability
- PC based router (zebra)
 - Performance depends upon main memory
- Commercial router (Cisco)
 - Limitation of CPU processing

Future Research Direction

- Better Route Reflector Architecture
 Cascade update v.s. Route Reflector
- 2. Further BGP related measurement
 - More complicated topology
 - Other BGP technique e.g. route flap dampening